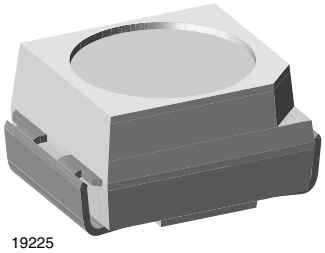
Standard SMD LED PLCC-2



19225

DESCRIPTION

This device has been designed to meet the increasing demand for white SMD LED.

The package of the VLMW41.. is the PLCC-2.

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled with a mixture of epoxy and TAG phosphor.

The TAG phosphor converts the blue emission partially to yellow, which mixes with the remaining blue to give white.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD PLCC-2
- Product series: standard
- Angle of half intensity: ± 60°

FEATURES

- High efficient InGaN technology
- Very narrow chromaticity coordinate group categorization according to CIE1931 per packing unit
- Typical color temperature 5500 K
- EIA and ICE standard package
- Compatible with reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020
- Available in 8 mm tape reel
- Preconditioning according tfo JEDEC® level 2a
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Camera flash light
- Signal and symbol luminaire
- Marker lights
- Interior and exterior automotive lighting: brake lights, turn lights, backlighting, side markers
- Indicator lighting

PARTS TABLE

PART	COLOR	LUMINOUS INTENSITY (mcd)			at I_F (mA)	COORDINATE (x, y)			at I_F (mA)	FORWARD VOLTAGE (V)			at I_F (mA)	TECHNOLOGY
		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		MIN.	TYP.	MAX.		
VLMW41S1T2-JKKL-08	White	180	275	450	10	-	0.30, 0.28	-	10	2.7	3.3	4.2	20	InGaN / sapphire and TAG
VLMW41S1T2-MKNL-08	White	180	275	450	10	-	0.33, 0.33	-	10	2.7	3.3	4.2	20	InGaN / sapphire and TAG

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C, unless otherwise specified)
VLMW41....-....

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
DC forward current	$T_{amb} \leq 80$ °C	I_F	20	mA
Surge forward current	$t_p \leq 10$ µs	I_{FSM}	0.1	A
Power dissipation		P_V	84	mW
Junction temperature		T_j	110	°C
Storage temperature range		T_{stg}	-40 to +100	°C
Operating temperature range		T_{amb}	-40 to +100	°C
Thermal resistance junction to ambient	Mounted on PC board (pad size > 16 mm²)	R_{thJA}	360	K/W

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OPTICAL AND ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)
VLMW41....-...., WHITE

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Luminous intensity	I_F = 10 mA	VLMW41S1T2...	I_V	180	275	450	mcd
Chromaticity coordinates according to CIE 1931	I_F = 10 mA	VLMW41S1T2-JKKL	x/y	-	0.30, 0.28	-	
		VLMW41S1T2-MKNL		-	0.33, 0.33	-	
Angle of half intensity	I_F = 10 mA		φ	-	± 60	-	°
Forward voltage	I_F = 20 mA		V_F	2.7	3.3	4.2	V
Temperature coefficient of V_F	I_F = 10 mA		TC_{VF}	-	-3	-	mV/K
Temperature coefficient of I_V	I_F = 10 mA		TC_{IV}	-	-0.4	-	%/K

Note

- Not designed for reverse operation

LUMINOUS INTENSITY CLASSIFICATION

GROUP	LIGHT INTENSITY (mcd)		
STANDARD	OPTIONAL	MIN.	MAX.
S	1	180	224
	2	224	280
T	1	280	355
	2	355	450

Note

- Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of ± 11 %.
  The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups are not be orderable.
  In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel.
  In order to ensure availability, single wavelength groups are not be orderable

CROSSING TABLE

VISHAY	OSRAM
VLMW41..	LWT67C..

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CHROMATICITY COORDINATED GROUPS FOR WHITE SMD LED					
	X	Y		X	Y
JK	0.2960	0.2590	ML	0.3189	0.3302
	0.2910	0.2680		0.3288	0.3452
	0.3005	0.2825		0.3288	0.3282
	0.3045	0.2715		0.3197	0.3131
JL	0.2910	0.2680	NK	0.3288	0.3081
	0.2850	0.2790		0.3288	0.3282
	0.2960	0.2955		0.3386	0.3426
	0.3005	0.2825		0.3386	0.3235
KK	0.3045	0.2715	NL	0.3288	0.3282
	0.3005	0.2825		0.3288	0.3453
	0.3100	0.2970		0.3386	0.3591
	0.3130	0.2840		0.3386	0.3426
KL	0.3005	0.2825	OK	0.3386	0.3235
	0.2960	0.2955		0.3386	0.3426
	0.3070	0.3120		0.3484	0.3571
	0.3100	0.2970		0.3484	0.3388
LK	0.3100	0.2970	OL	0.3386	0.3426
	0.3197	0.3131		0.3386	0.3591
	0.3205	0.2956		0.3484	0.3730
	0.3130	0.2840		0.3484	0.3571
LL	0.3070	0.3120	PK	0.3484	0.3388
	0.3189	0.3302		0.3484	0.3571
	0.3197	0.3131		0.3582	0.3715
	0.3100	0.2970		0.3582	0.3542
MK	0.3197	0.3131	PL	0.3484	0.3571
	0.3288	0.3282		0.3484	0.3730
	0.3288	0.3081		0.3582	0.3792
	0.3205	0.2956		0.3582	0.3715

Note

- Chromaticity coordinate groups are tested at a current pulse duration of 25 ms and a tolerance of ± 0.01.

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

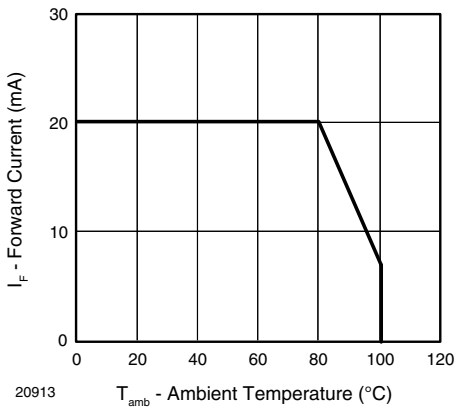


Fig. 1 - Forward Current vs. Ambient Temperature

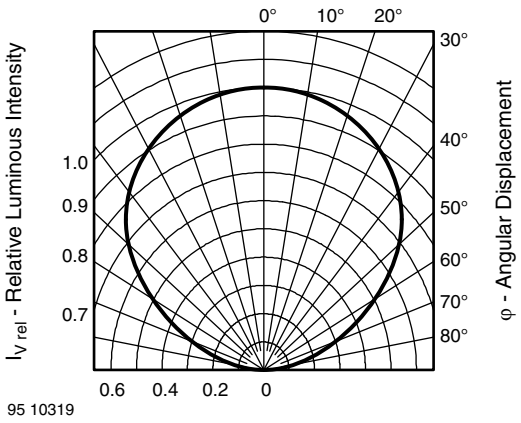


Fig. 2 - Relative Luminous Intensity vs. Angular Displacement

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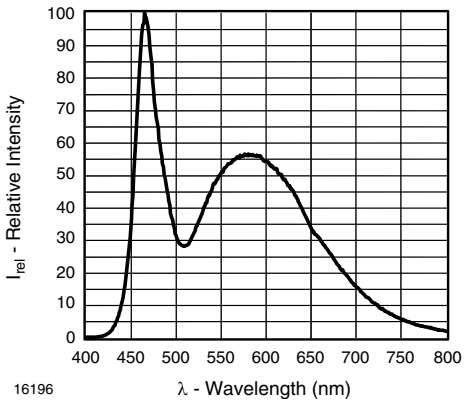


Fig. 3 - Relative Intensity vs. Wavelength

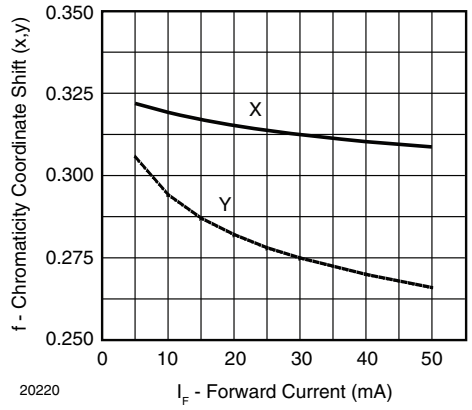


Fig. 6 - Chromaticity Coordinate Shift vs. Forward Current

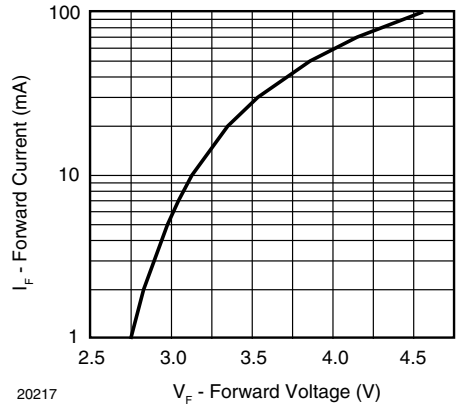


Fig. 4 - Forward Current vs. Forward Voltage

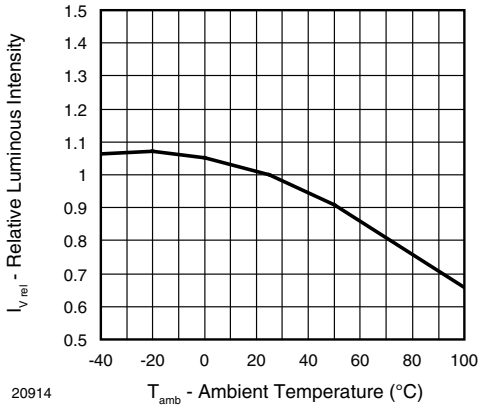


Fig. 7 - Relative Luminous Intensity vs. Ambient Temperature

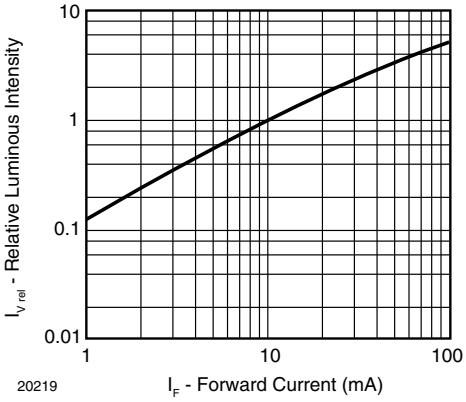


Fig. 5 - Relative Luminous Intensity vs. Forward Current

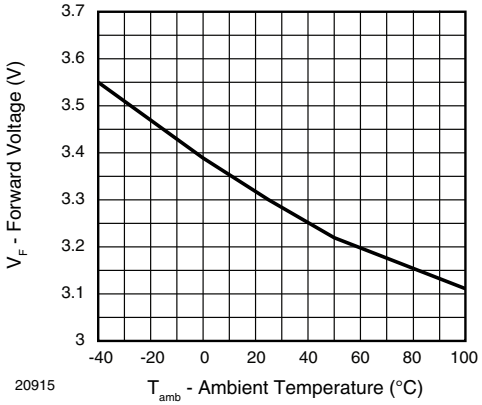


Fig. 8 - Forward Voltage vs. Ambient Temperature

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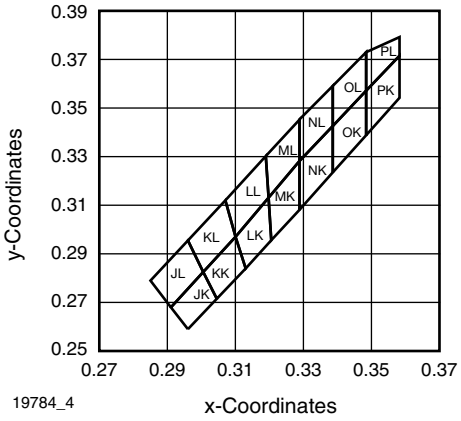
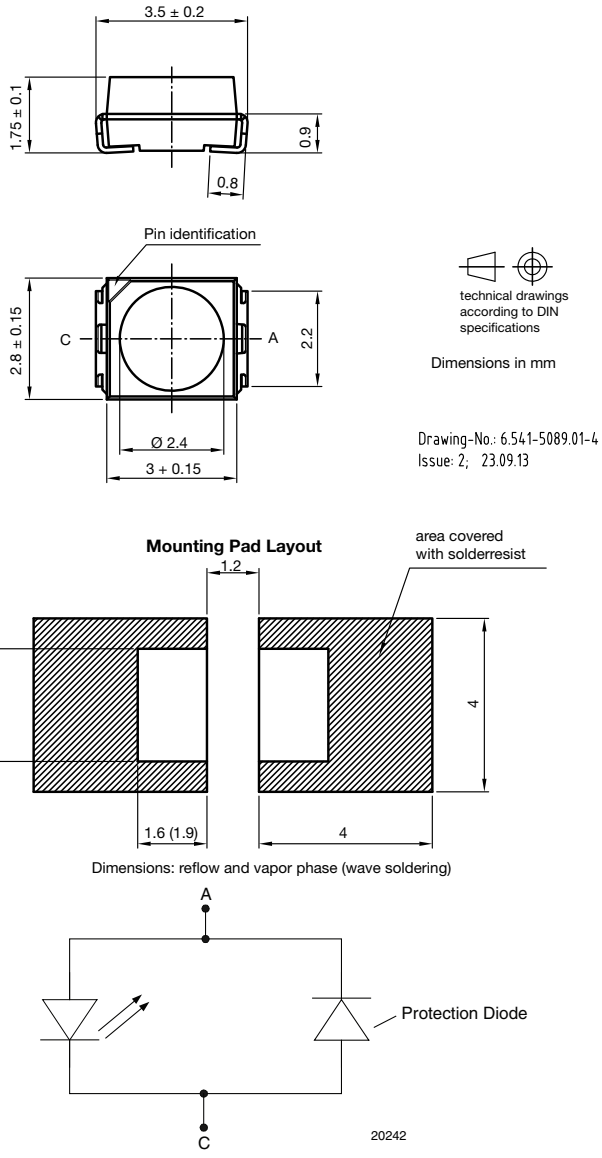


Fig. 9 - Coordinates of Colorgroups

PACKAGE DIMENSIONS in millimeters

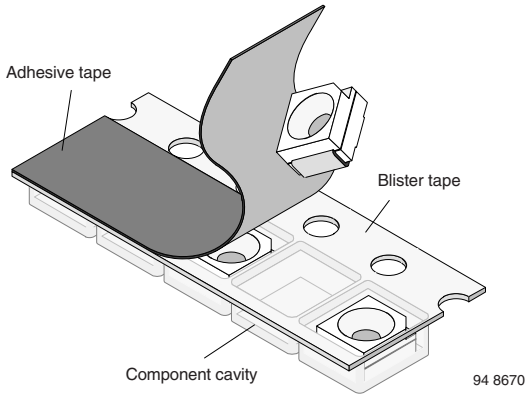


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METHOD OF TAPING / POLARITY AND TAPE AND REEL

SMD LED (VLM.3..., VLM.4...-SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



TAPING OF VLM.3..., VLM.4...

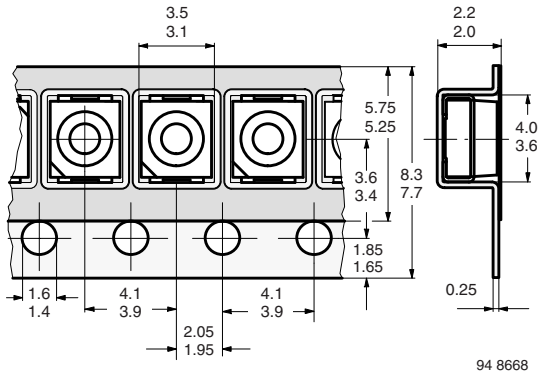


Fig. 10 - Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS.)

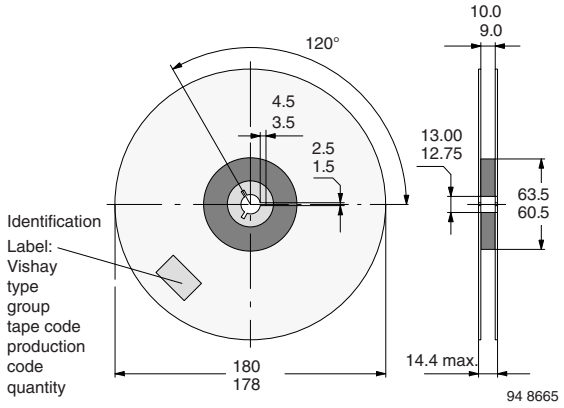


Fig. 11 - Reel Dimensions - GS08

SOLDERING PROFILE

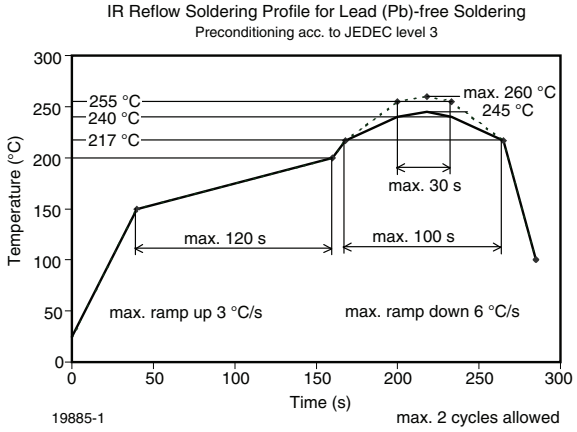


Fig. 12 - Vishay Lead (Pb)-free Reflow Soldering Profile (acc. to J-STD-020)

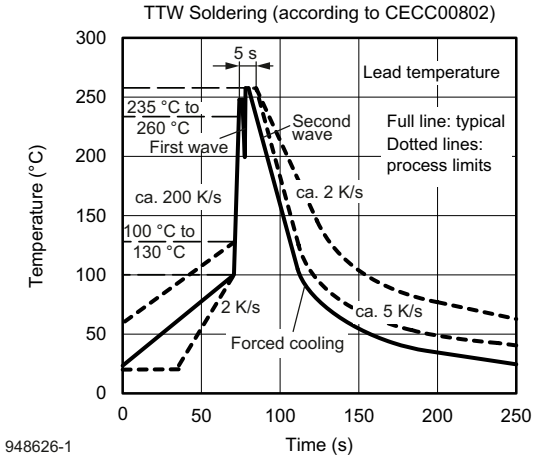


Fig. 13 - Double Wave Soldering of Opto Devices (all Packages)

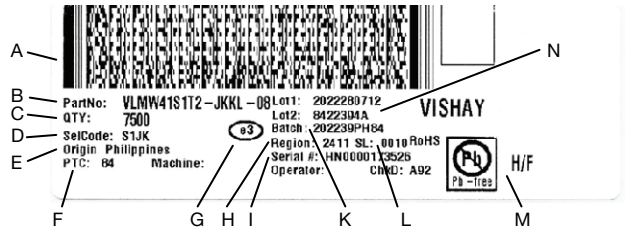
PACKING INFORMATION			
PART	PIECES PER REEL	REELS PER BOX	MOQ (1)
VLMW41....-....-08	1500	5	7500

Note

(1) MOQ = minumum order quantity

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BAR CODE PRODUCT LABEL (example)



A. 2D barcode

B. Part No: Vishay part number

C. QTY: quantity

D. SelCode: selection bin code

E. Country of origin

F. PTC: production plant code

G. Termination finish

H. Region code

I. Serial#: serial number

K. Batch number: year, week, country code, plant code

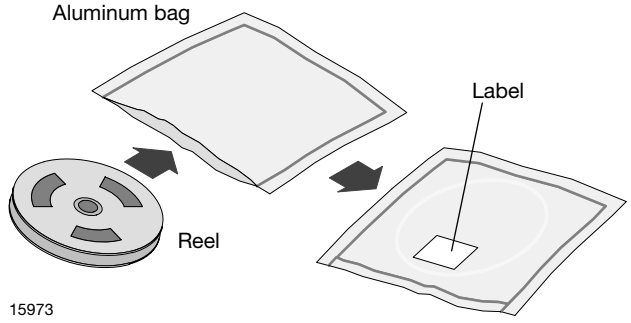
L. SL: storage location

M. Environmental symbols: RoHS, lead (Pb)-free, halogen-free

N. Lot numbers

DRY PACKING

The reel is packed in an anti-humidity bag to protect the devices from absorbing moisture during transportation and storage.



FINAL PACKING

The sealed reel is packed into a cardboard box. A secondary cardboard box is used for shipping purposes.

RECOMMENDED METHOD OF STORAGE

Dry box storage is recommended as soon as the aluminum bag has been opened to prevent moisture absorption. The following conditions should be observed, if dry boxes are not available:

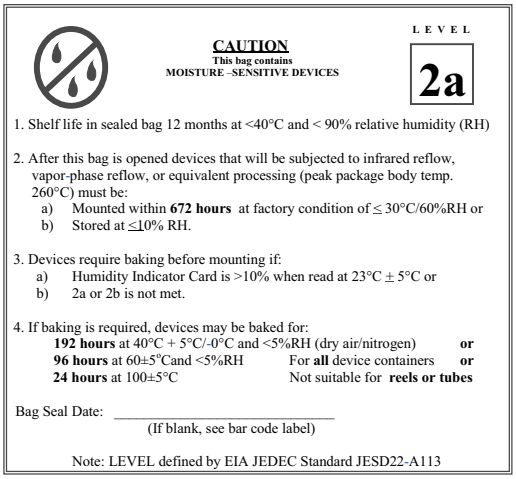
- Storage temperature 10 °C to 30 °C
- Storage humidity ≤ 60 % RH max.

After more than 672 h under these conditions moisture content will be too high for reflow soldering.

In case of moisture absorption, the devices will recover to the former condition by drying under the following condition:

192 h at 40 °C + 5 °C / - 0 °C and < 5 % RH (dry air / nitrogen) or
96 h at 60 °C + 5 °C and < 5 % RH for all device containers or
24 h at 100 °C + 5 °C not suitable for reel or tubes.

An EIA JEDEC standard JESD22-A112 level 2a label is included on all dry bags.



Example of JESD22-A112 level 2a label

ESD PRECAUTION

Proper storage and handling procedures should be followed to prevent ESD damage to the devices especially when they are removed from the antistatic shielding bag. Electrostatic sensitive devices warning labels are on the packaging.

VISHAY SEMICONDUCTORS STANDARD BAR CODE LABEL

The Vishay Semiconductors standard bar code labels are printed at final packing areas. The labels are on each packing unit and contain Vishay Semiconductors specific data.

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